

FIG. 1

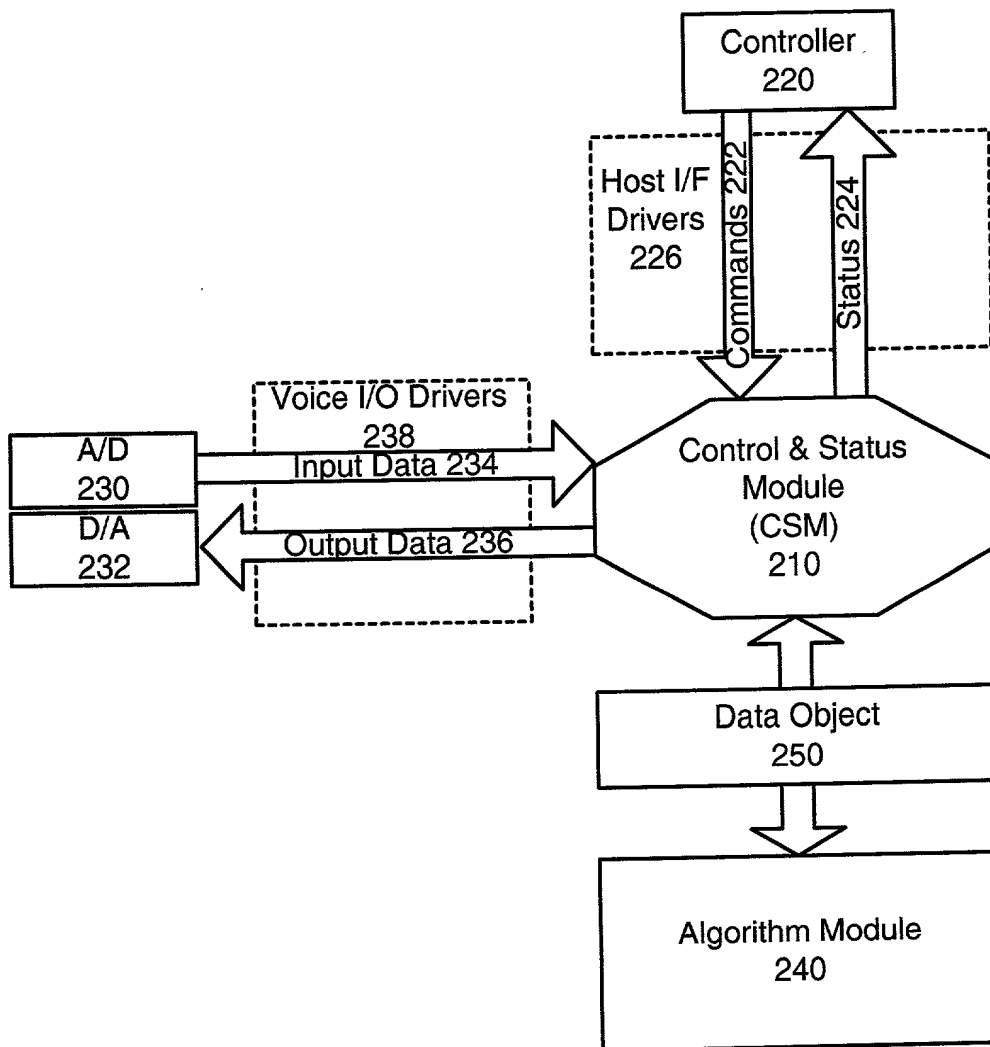


FIG. 2

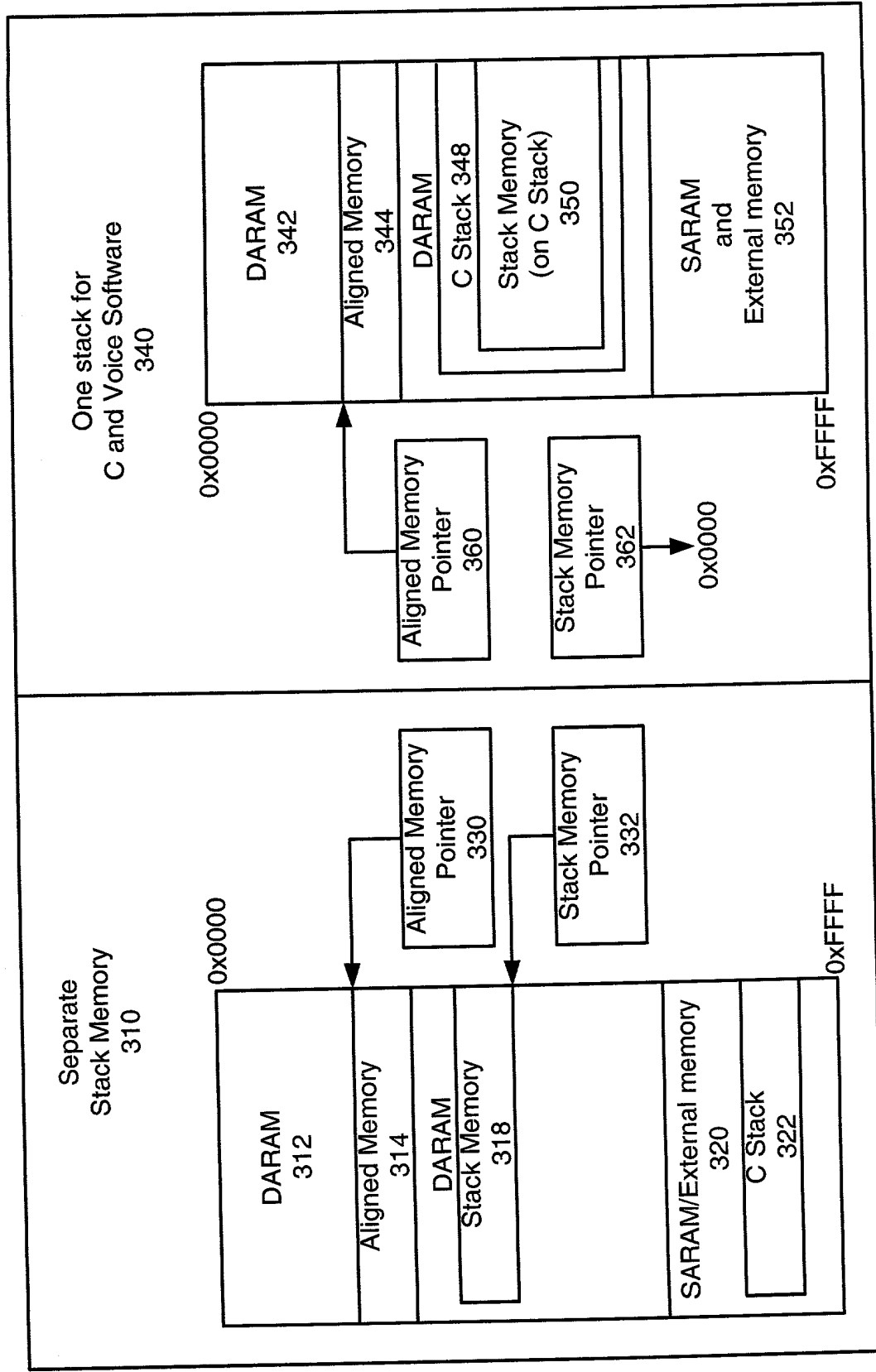


FIG. 3

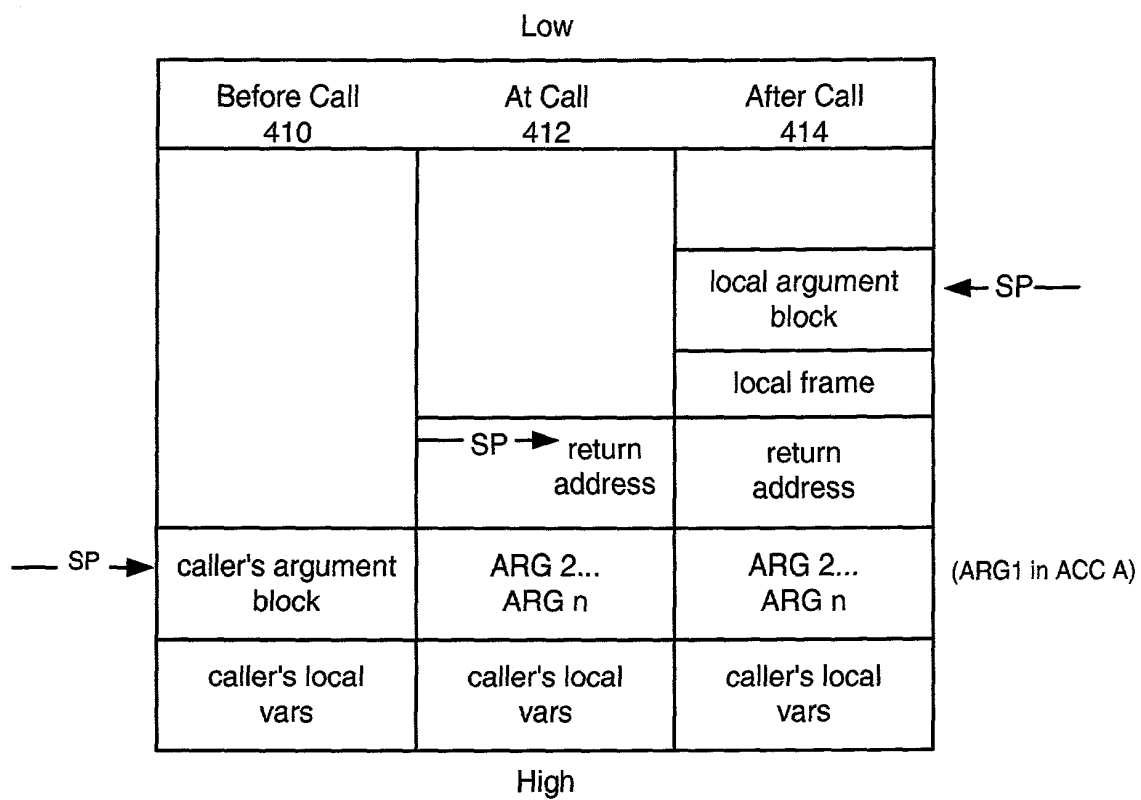


FIG. 4

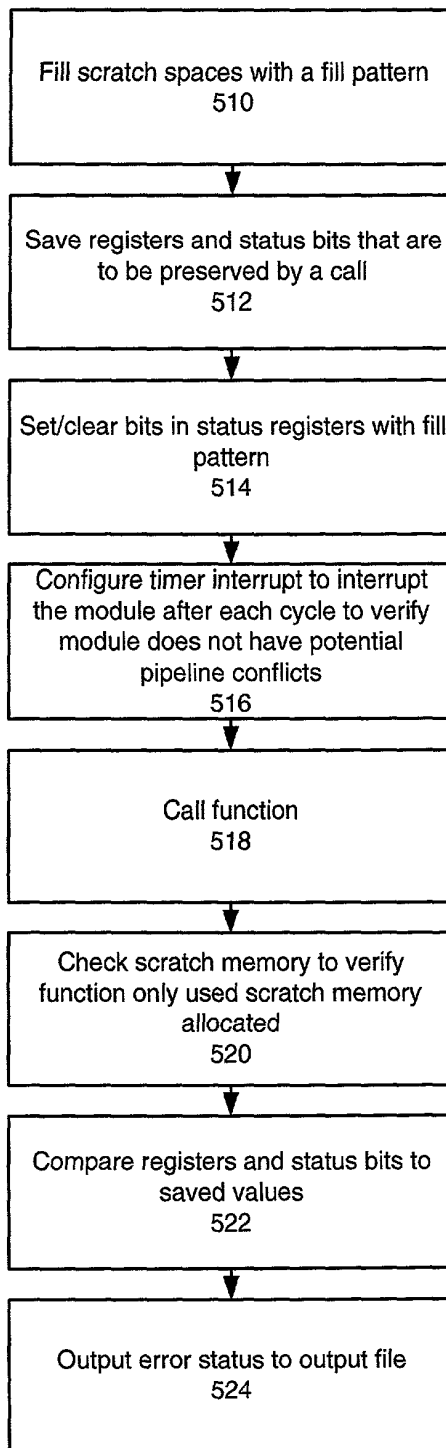


FIG. 5

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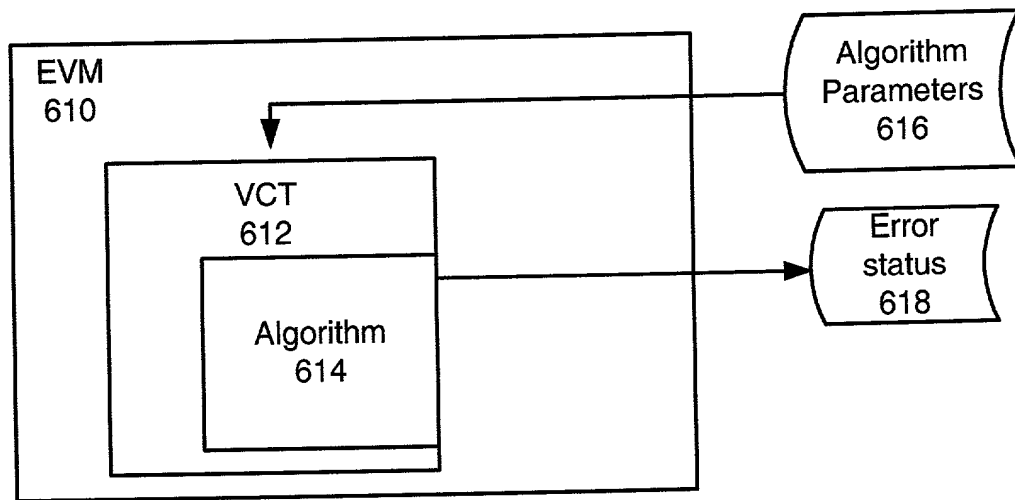


FIG. 6

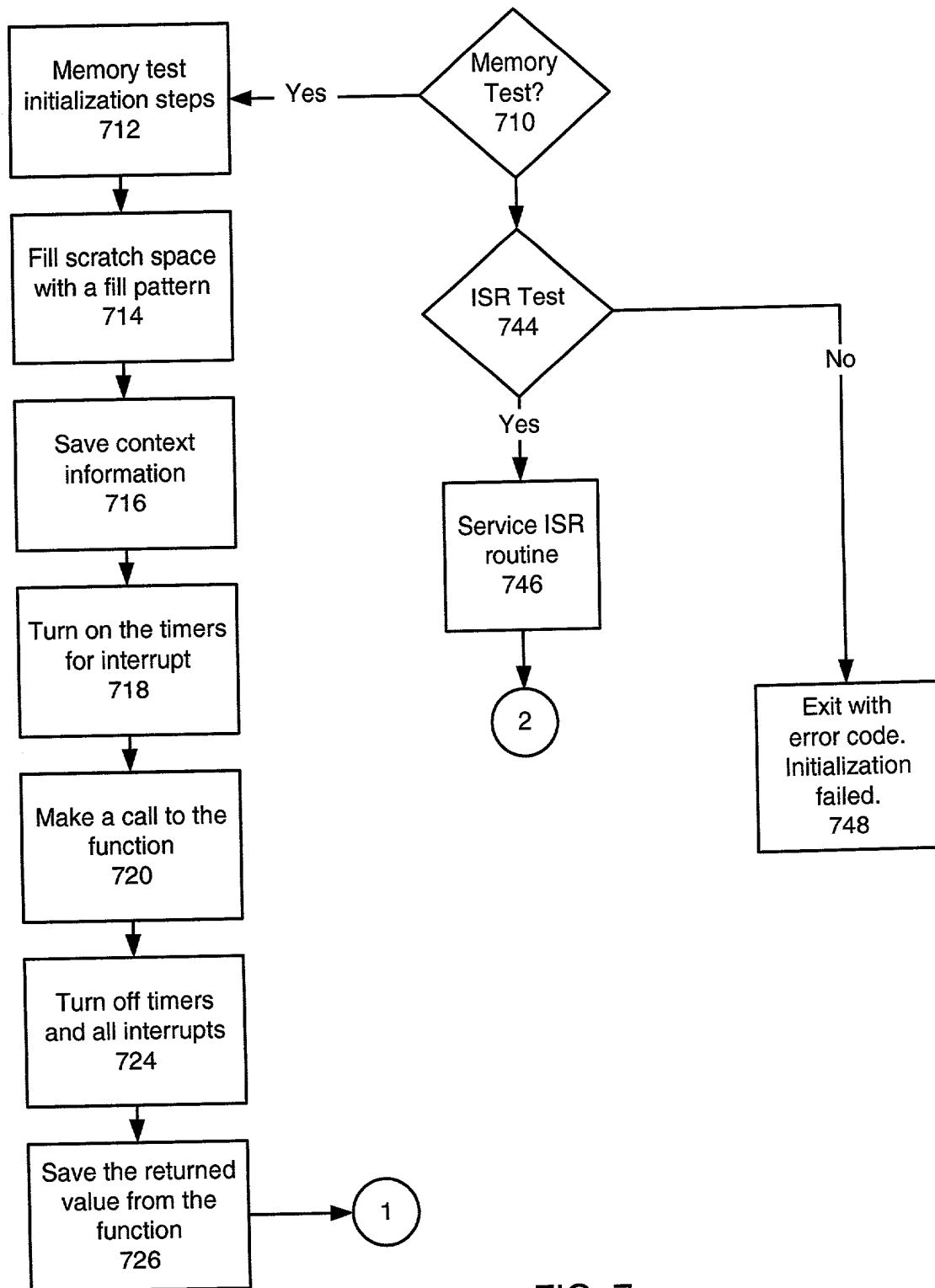


FIG. 7a

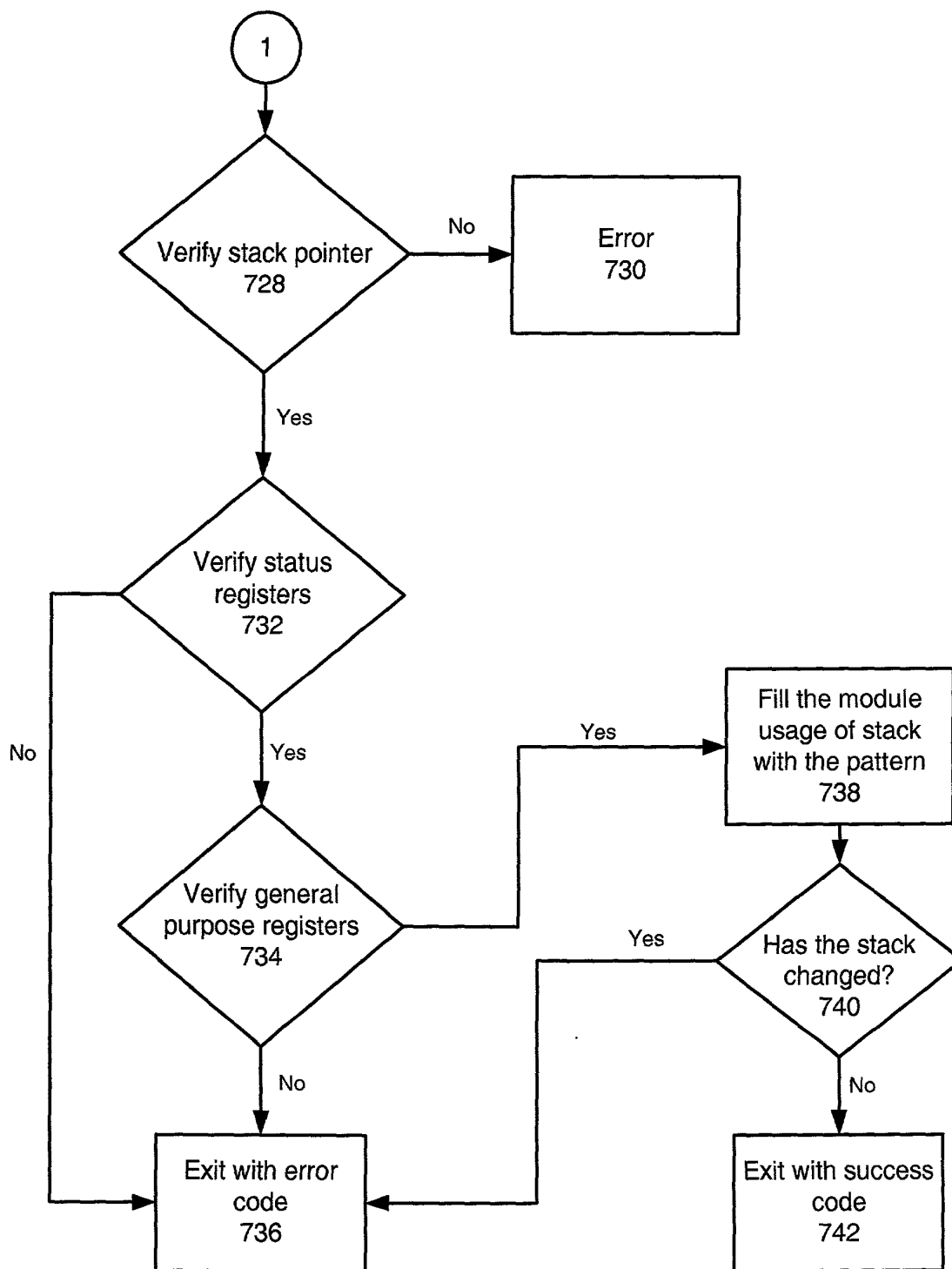


FIG. 7b


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graph TD
    810[Copy timer vector to ISR table  
810] --> 812[Turn off timer in timer control registers  
812]
    812 --> 814[Initialize timer period registers  
814]
    814 --> 816[Clear any pending timer interrupts  
816]
    816 --> 818[Enable timer interrupt  
818]
    818 --> 820[Skip timer interrupt vector and timer ISR  
820]

```

FIG. 8

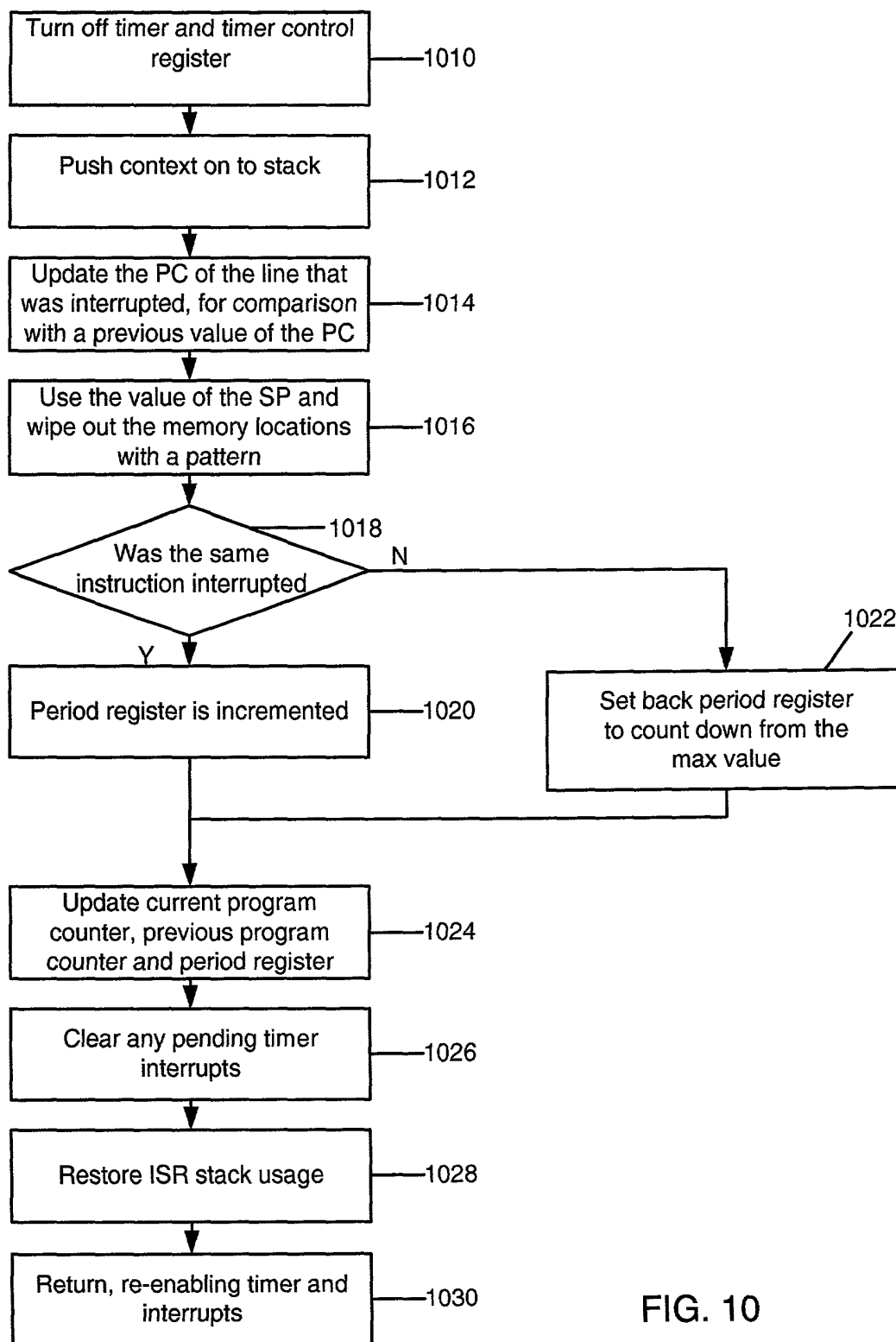


FIG. 10

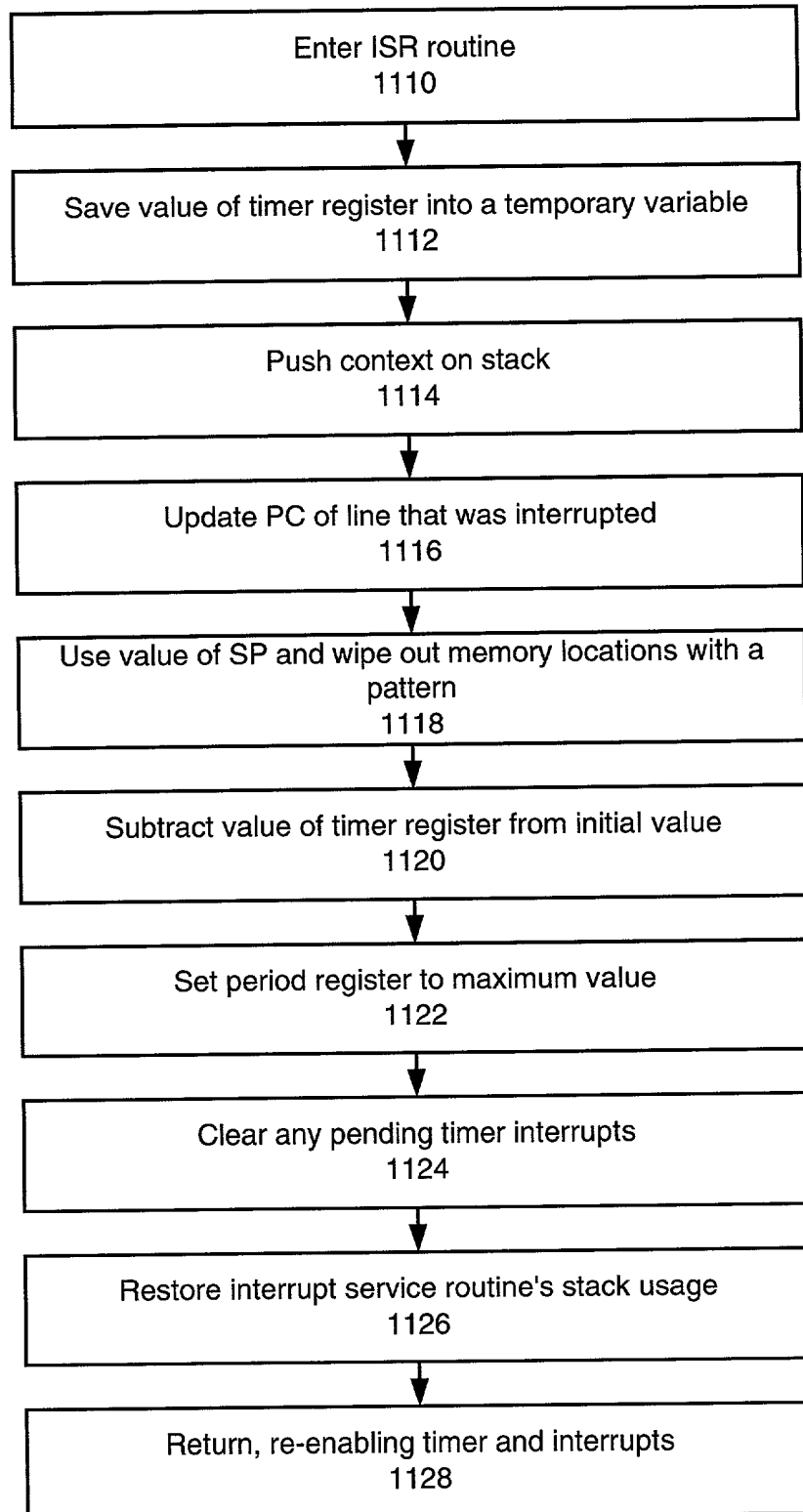


FIG. 11